PATENT

p. 4

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of

Van Dyke et al.

Application No.: 10/657,957

Filed: 09/08/03

For: SYSTEM AND METHOD FOR A HIGH BANDWIDTH-LOW LATENCY

MEMORY CONTROLLER

Group Art Unit: 2186

Examiner: Choi, Woo H.

Docket No.

NVIDP033A\_P000873

Date: January 6, 2005

## CERTIFICATE OF FACSIMILE

1 hereby certify that this correspondence is being feesimily transmitted to the Commissioner for Papers, Alexandria VA-22313-1450 attn: Examiner fami at besimile aumber

Signed

Erica L. Farlow

## **AMENDMENT B**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to an Office Action mailed 11/12/04, please enter the following in the above application:

01/26/2005 DHALL1

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## PATENT APPLICATION FEE DETERMINATION RECORD

Application or Docket Number

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